



Tutorial 2A: New Approaches to Flash Memory Subsystems

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Agenda

- Extending the NVMeHCI Standard to the Enterprise
 - Amber Huffman, Intel
- Improving System Performance and Longevity with a New NAND Flash Architecture
 - Jin-Ki Kim, MOSAID
- 3Bit Per Cell NAND Flash
 - Terry Grunzke, Micron Technology
- Toggle-mode NAND to Fill Growing Need for Higher Performance
 - Harry Yoon, Samsung Semiconductor
- NAND Flash Architecture and Specification Trends
 - Michael Abraham, Micron Technology

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EXTENDING THE NVMHCI STANDARD TO THE ENTERPRISE

Amber Huffman, Intel

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IMPROVING SYSTEM PERFORMANCE AND LONGEVITY WITH A NEW NAND FLASH ARCHITECTURE

Jin-Ki Kim, MOSAID

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3BIT PER CELL NAND FLASH

Terry Grunzke, Micron Technology

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TOGGLE-MODE NAND TO FILL GROWING NEED FOR HIGHER PERFORMANCE

Harry Yoon, Samsung Semiconductor

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NAND FLASH ARCHITECTURE AND SPECIFICATION TRENDS

Michael Abraham, Micron Technology

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